

Fig. 1  
(PRIOR ART)

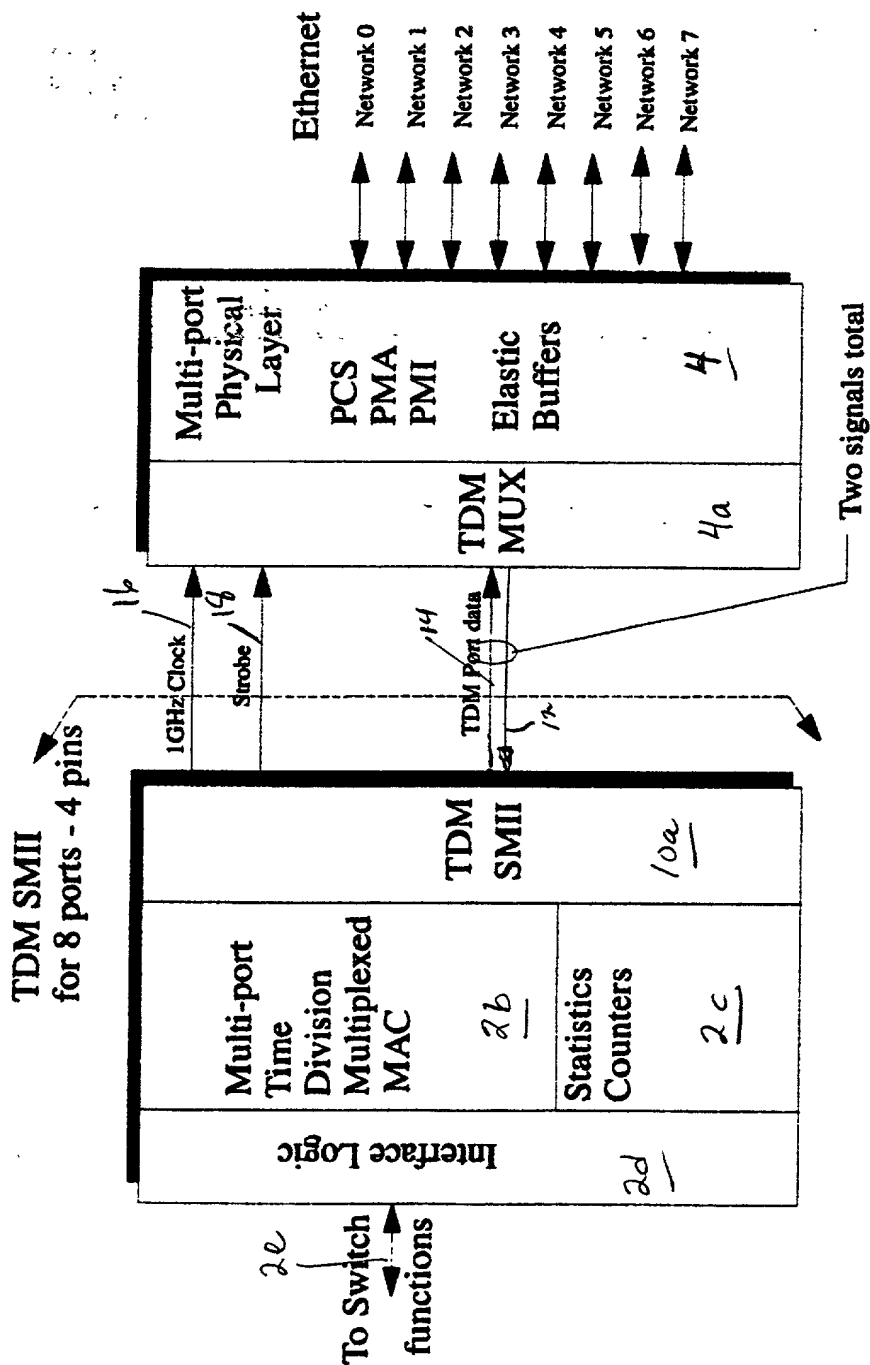


Fig. 2

	CLOCK					CLOCK						
	0	1	2	3	...	n	n+1	n+2	n+3	n+4	...	2n+1
network 0	X						X					X
network 1		X						X				
network 2			X						X			
network 3				X						X		
...					...							
network n						X					X	
strobe	X						X					X

Fig. 3